## Test \#1

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Electronic Circuits (II)
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## Prob. 1 (10)

We want to design the source follower shown in Fig. 1 to have voltage gain of 0.8. Using $\frac{\mathrm{W}}{\mathrm{L}}=\frac{30}{0.18}, \mu_{\mathrm{n}} C_{o x}=200 \mu A / V^{2}, \mathrm{~V}_{\text {th }}=0.4$ and $\lambda=0$, determine the required gate bias voltage.


Fig. 1

## Prob. 2 (20)

Determine the voltage gain of the following CS with source degeneration amplifiers shown in Fig. 2. Assume $\lambda=0$. Express your answers in terms of MOS transconductances and Ro. (4 points each)

(a)

(b)

(c)

(d)

(e)

Fig. 2

## Prob. 3 (30)

Current mirrors are often used as current sources. For better performing current sources, its output resistance should be as large as possible. In this problem, you are asked to determine the output resistance of a current-source circuit shown below. All MOS transistors are identical with $\frac{\mathrm{w}}{\mathrm{L}}=100, \mathrm{~V}_{\mathrm{th}}=0.7 \mathrm{~V}$, and $\mu_{\mathrm{n}} C_{o x}=200 \mu \mathrm{~A} / \mathrm{V}^{2}, \mathrm{r}_{\mathrm{o}}=200 \mathrm{k} \Omega$. Use $\mathrm{V}_{\mathrm{DD}}=3 \mathrm{~V}$ and assume all transistors are in saturation.

(a)(10) Determine the numerical value for $R$ so that $I_{0}=100 \mu \mathrm{~A}$. For this part, ignore the influence of $r_{0}$.
(b)(10) The small-signal model for Block $A$ is a resistor. Determine the expression for its resistance, $\mathrm{R}_{\text {eq }}$. Your answer should contain only $\mathrm{gm}_{\mathrm{m}}$ and $\mathrm{r}_{\mathrm{o}}$.
(c)(10) Determine the expression for output resistance, $\mathrm{R}_{\text {out }}$. Use the result obtained in (b). Your answer should contain only $g_{m}$ and $r_{0}$.

## Prob. 4 (20)

Determine the expression for $R_{p}$ in the circuit of Fig. 4 such that $I_{1}=I_{R E F} / 2$. Your answer should contain only $\frac{\mathrm{w}}{\mathrm{L}}, I_{R E F}, \beta=\mu_{n} C_{o x}$. Assume both MOS transistors are in saturation and $\lambda=0$.


Fig. 4

## Prob. 5 (20)


(a)

(b)

Fig. 5

For each of cascade circuits shown in Fig. 5, (a) determine the output impedance, (b) voltage gain. Assume each transistor $\mathrm{M}_{\mathrm{n}}$ has output impedance $r_{0, n}$, which is much larger than $1 / g_{m, n}$ so that $g_{m, n r o n} \gg 1$.

