= (Razavi 9.1, 9.2) —

Various bias techniques for MOS circuits



How can we make a constant current source with MOS?



Constant current source:



$$I_{D1} = \frac{1}{2} \mu_n C_{ox} \left(\frac{W}{L}\right)_1 \left(V_{GS} - V_{TH}\right)^2$$

$$I_{D1} = I_{REF} = \frac{V_{DD} - V_{GS}}{R}$$

$$I_{O} = I_{D2} = \frac{1}{2} \mu_{n} C_{ox} \left(\frac{W}{L}\right)_{2} \left(V_{GS} - V_{TH}\right)^{2}$$

$$\frac{I_o}{I_{REF}} = \frac{\left(W / L\right)_2}{\left(W / L\right)_1}$$

Limitation on
$$V_o$$
? $V_O \ge V_{GS} - V_{TH}$



Mismatches between $\mathsf{I}_{\mathsf{REF}}$ and I_{O}



For
$$Q_1$$
 and Q_2
 $I_0 = I_{REF}$ only if $V_{DS1} = V_{DS2} \rightarrow V_0 = V_{GS}$

As $V_{\rm O}$ increases, $I_{\rm O}$ increases from $I_{\rm REF}$

$$I_o = I_{REF} + \frac{V_o - V_{GS}}{r_0}$$



(Without r₀ consideration)



$$I_{2} = I_{REF} \frac{\left(W/L\right)_{2}}{\left(W/L\right)_{1}}$$
$$I_{3} = I_{REF} \frac{\left(W/L\right)_{3}}{\left(W/L\right)_{1}}$$
$$I_{3} = I_{4}$$
$$I_{5} = I_{4} \frac{\left(W/L\right)_{5}}{\left(W/L\right)_{4}}$$















 $A_{\!\scriptscriptstyle v}$ for Cascode Amplifier



- Can be viewed as CS+CG



 A_V : - $g_{m1} g_{m2} r_{O2} r_{O1}$

→ Effectively CS amplifier with $R_{out} = g_{m2}r_{O2}r_{O1}$

Larger gain or faster for the same gain

Less V_{out} range



With cascoded PMOS transistors for the current source



$$R_{on} \approx g_{m2} r_{O2} r_{O1}$$
$$R_{op} \approx g_{m3} r_{O3} r_{O4}$$

 $A_{V}: - g_{m1} (g_{m2} r_{O2} r_{O1} || g_{m3} r_{O3} r_{O4})$



Homework(1)

Determine I_{copy} in following circuits. Assume all MOS transistors are in saturation and the influence of r_0 can be ignored.





Homework(2)

Determine the output impedance of circuits shown below. All transistors are in saturation and $g_m r_0 >> 1$



