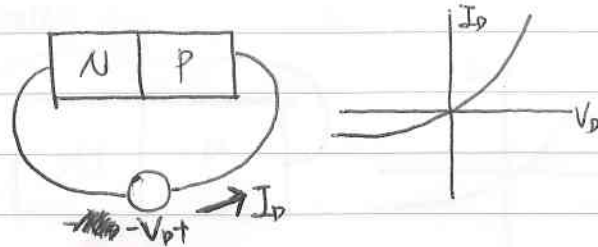


Lect 3.

Basics of Si device. PN Junction



전공정태

$$\vec{F} = q\vec{E} \rightarrow \text{힘이 있으므로 } \vec{a} = \frac{\vec{F}}{m}$$

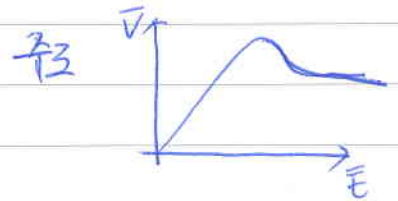
즉 전류가 흐르기 시작.

반도체 안에서 Si는 가득 차있으므로 damping force가 많음 (가속은 생기지 않음)
 $\rightarrow$ 하지만 전기장 방향으로 움직인다. $\vec{v} = \mu \vec{E}$. (μ : $\text{cm}^2/\text{V}\cdot\text{s}$)

Si 특성

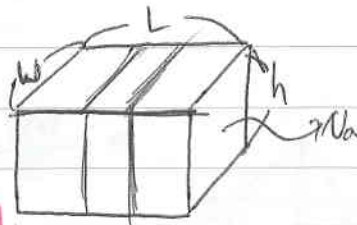
$$\mu_p \sim 400$$

$$\mu_n \sim 1350$$



$$I = \frac{dQ}{dt}$$

$$= \frac{q \cdot V_{\Delta t} \cdot W \cdot h \cdot n}{\Delta t}$$



$$J = \frac{I}{Wh} = q \cdot \mu_p \cdot p \cdot E \rightarrow J = \sigma E \quad [\sigma = \frac{1}{\rho}]$$

$\rho = \frac{1}{\sigma}$ resistivity [$\Omega \cdot \text{cm}$]

$$R = \frac{V}{I} = \frac{E \cdot L}{J \cdot A} = \frac{\rho \cdot L}{A}$$

$$V = IR$$

$$J = \sigma E \quad (\text{주3상관x})$$

$$\Rightarrow J_{\text{total}} = J_p + J_n = q(\mu_p p + \mu_n n)E$$

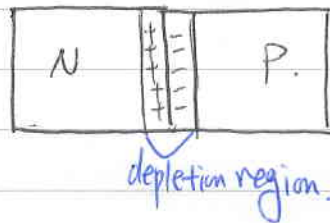
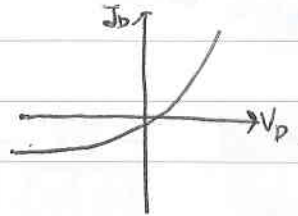
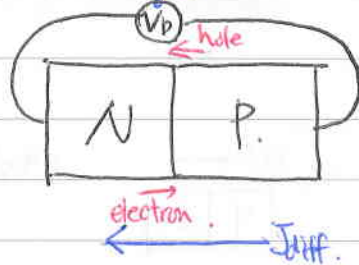
$$J_{\text{diffusion}} = qD_p \frac{dp}{dx} + qD_n \frac{dn}{dx} \Rightarrow \vec{J} = -qD_p \nabla p + qD_n \nabla n$$

[D]: diffusion coefficient: $\frac{\text{cm}^2}{\text{sec}} \cdot \frac{1}{\text{cm}^2} \cdot \text{cm}^4 = \frac{\text{cm}^2}{\text{sec}}$

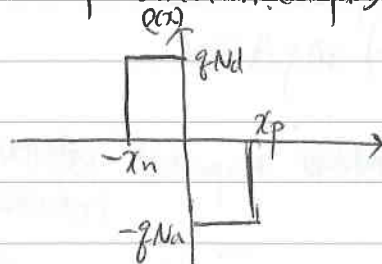
$$\frac{U}{D} = \frac{kT}{q} \rightarrow \text{Thermal Energy (室温에서의 Carrier 가짐 있는 평균 운동 E).}$$

$$\frac{kT}{q} \left[\frac{eV}{e} \right] : \text{Thermal Voltage} : 26\text{mV}.$$

Einstein Equ.

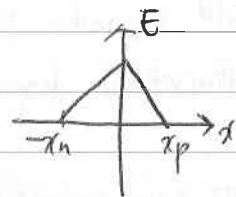
외부 Voltage가 없으면 $J_{diff} + J_{diff} = 0$

Abrupt Junction (step junction)



$$N_d x_n = N_a x_p$$

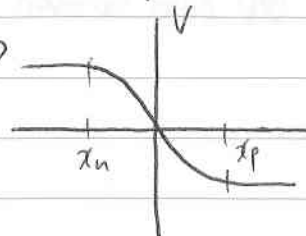
$$\nabla E = \rho, \quad \frac{dE}{dx} = \frac{\rho(x)}{\epsilon} \Rightarrow$$



$$-x_n < x < 0, \quad E(x) = \frac{qN_d}{\epsilon} (x - x_n)$$

$$0 < x < x_p, \quad E(x) = -\frac{qN_a}{\epsilon} (x - x_p)$$

$$E(x) = -\frac{dV}{dx}$$

 $\Rightarrow$ 

부註: potential의 높음(x) → 낮음(x)로
가는데 E-field도 바로

$$-x_n < x < 0,$$

$$\Rightarrow V(x) = V_n - \frac{qN_d}{2\epsilon} (x + x_n)^2$$

$$0 < x < x_p$$

$$\Rightarrow V(x) = V_p + \frac{qN_a}{2\epsilon} (x_p - x)^2$$

$$V_n - V_p = \frac{qN_d}{2\epsilon} x_n^2 + \frac{qN_a}{2\epsilon} x_p^2$$

$$J_{p,drift} + J_{p,diff} = 0$$

$$-qD_p \frac{dp}{dx} + q\mu_p pE = 0$$

$$D_p \frac{dp}{dx} = \mu_p pE$$

$$\int_{\frac{n_i^2}{N_a}}^{N_d} D_p \frac{dp}{p} = \int_{-x_n}^{x_p} \mu_p E dx$$

$$\rightarrow D_n \left(\ln N_a - \ln \frac{n_i^2}{N_d} \right) = D_n \ln \cdot \frac{N_a N_d}{n_i^2} = \mu_p [V(x) - V(x_p)]_{-x_n}^{x_p}$$

$$= \mu_p (V(x = -x_n) - V(x = x_p))$$

$$V_{bi} = V_n - V_p = \frac{kT}{q} \ln \frac{N_a N_d}{n_i^2}$$

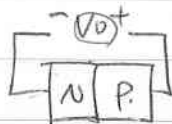
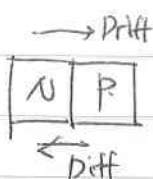
→ 내부전압이 있으면 $(V_{bi} - V_0)$ 로 쓴다.

$$x_n + x_p = W_p = \left[\frac{2\epsilon}{q} V_{bi} \left(\frac{1}{N_a} + \frac{1}{N_d} \right) \right]^{1/2}$$

$$x_p = W_p \frac{N_d}{N_a + N_d} \quad x_n = W_p \frac{N_a}{N_a + N_d}$$

// equilibrium 관계식

$V_0 \neq 0$



$V_0 > 0$ 인 경우: more diffusion, 균형상태

↳ potential 장벽이 낮아지면 양쪽 (p, n)의 농도차가 매우 크므로 급격하게 크다.

$V_0 < 0$ 인 경우: drift가 커져야 되는데 여기

위에서 원쪽(n)이 hidden

오른쪽(p)이 electron이 많아 되기 때문

도움이 그렇지 않음으로: 전류 조금만

$$I_0 = I_s \exp\left(\frac{V_0}{V_T} - 1\right)$$

$$I_s = A q n_i \left(\frac{D_n}{N_a L_n} + \frac{D_p}{N_d L_p} \right)$$

변력터: Voltage가 바뀌면 Cap이 바뀌는 것 (Varactor).

Capacitor: Voltage를 가했음으로 charge가 바뀌는 거.

이런 PN junction은 charge가 uniform하지만 거리가 달라진다.

$$C = \frac{\epsilon \cdot A}{W_p} \quad \text{Reverse bias 일때 Cap이 작아진다.}$$