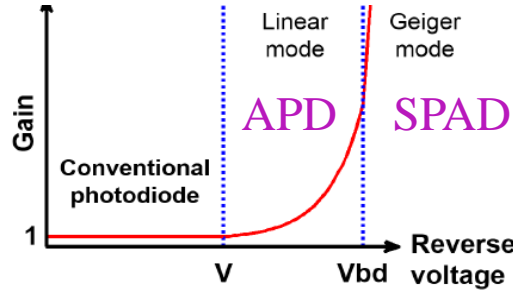
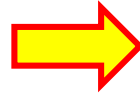
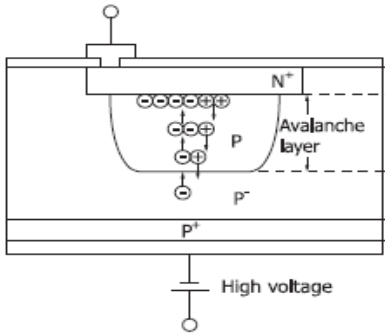
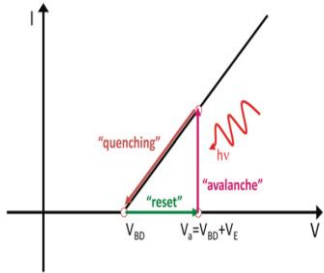
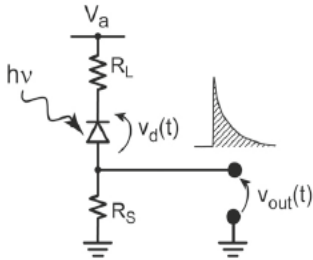


2020-2~2022-1 Works

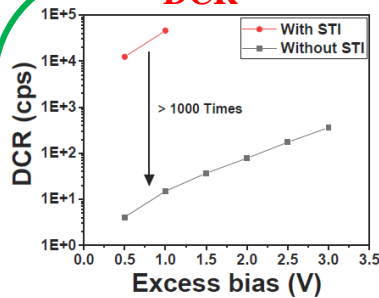
SPAD



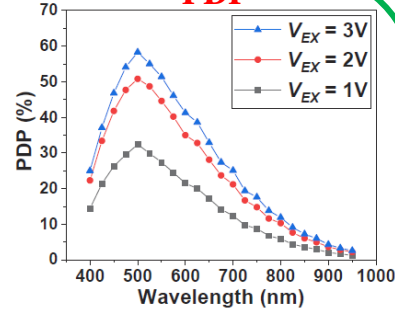
Quenching Circuit



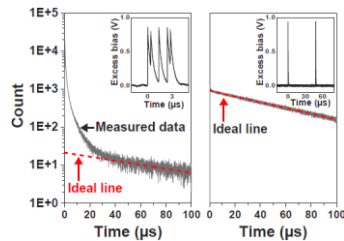
DCR



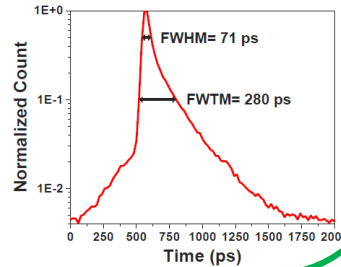
PDP



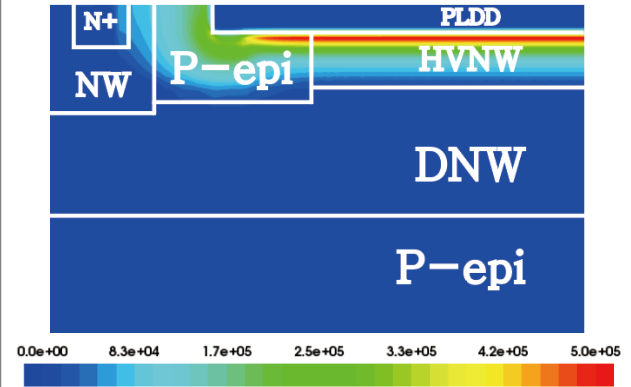
Afterpulsing probability



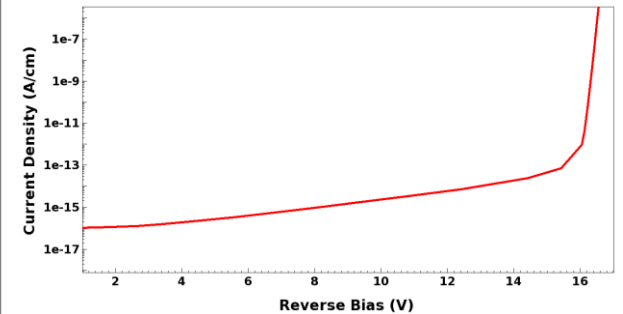
Time jitter



TCAD Simulation



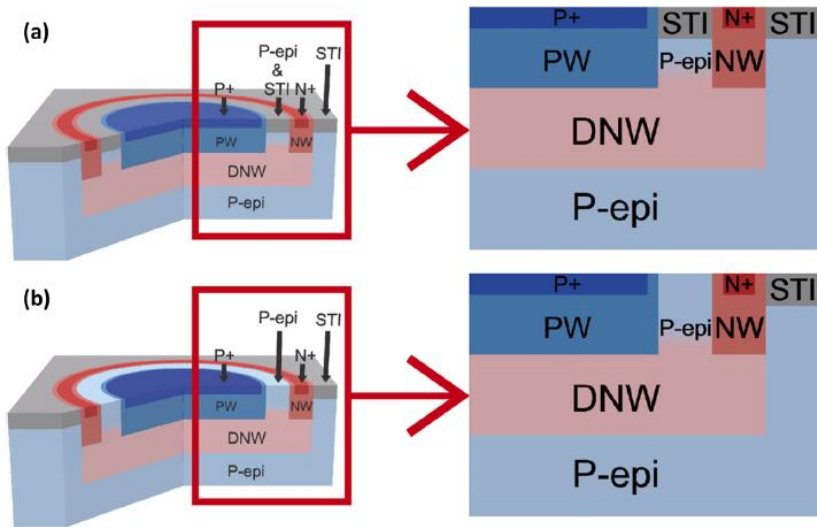
Electric field magnitude linear (V/cm)



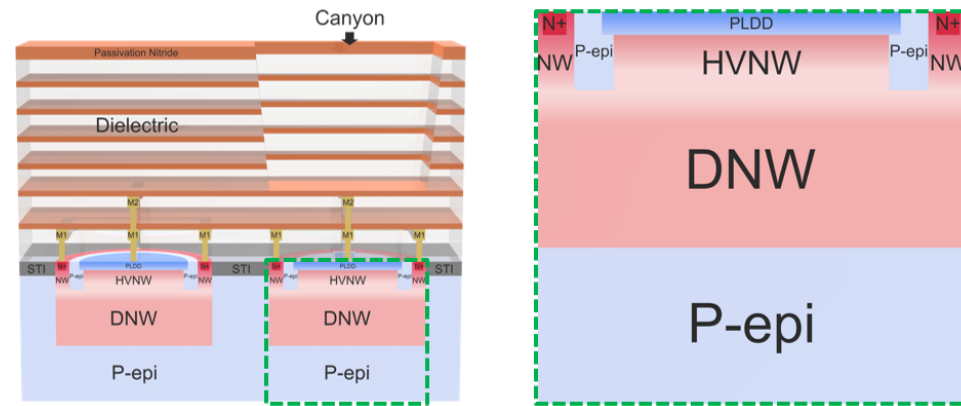
Current density – voltages profile of the SPAD

Publication

<Accepted>



<Submitted>



Won-Yong Ha et al. “Noise optimization of single-photon avalanche diodes fabricated in 110 nm CMOS image sensor technology”, *Optics Express (Editors’ Pick)*

Won-Yong Ha et al. “SPAD Developed in 55 nm BCD Technology Achieving Near 90% Peak PDP”, *IEDM*

2022 Plan

