

THE 29TH KOREAN CONFERENCE ON SEMICONDUCTORS

# 제 29회 한국반도체학술대회

2022. 1. 24(월) - 26(수)

강원도 하이원 그랜드호텔(컨벤션타워) **Online & Offline Hybrid**

제 29회 한국반도체학술대회가 온/오프라인 하이브리드로 개최될 예정입니다. 제 29회 한국반도체학술대회 조직위원회와 사무국은 현장 참가자의 안전을 위해 코로나19 방역 지침에 따라 일차 별 현장 참석 인원을 선착순으로 제한합니다.

현장에 참여 예정이신 참가자께서는 참석 인원 초과 시, 숙소 등 행사장 이외의 공간에서 온라인으로 참석하실 수 있으니 본 학술대회가 안전하게 마무리 될 수 있도록 적극 협조 부탁드립니다.

(\*\* 방역 당국의 지침에 따라 변동 될 수 있습니다)

개회식 & 기조강연 유튜브 ▶

현장참가자 코로나19 대응지침 ▶

**ONLINE LIVE STREAMING**

**개회식**  
1월 25일(화) 13:50-14:00

**기조강연**

기조강연1 / 1월 25일(화) 14:00-14:50  
**Memristive Neuromorphic Technology**  
강성모 교수 (UC 샌디에고)

기조강연2 / 1월 25일(화) 15:00-15:50  
**인공지능과 반도체: 새로운 이상의 기반**  
**최기영 전 과기정통부 장관** (서울대학교 ( 명예교수))

분과	포스터세션 LIVE CHAT 일정
A. Interconnect & Package	26일(수), 09:00-11:00
B. Patterning	26일(수), 09:00-11:00
C. Material Growth & Characterization	26일(수), 14:00-16:00
D. Thin Film Process Technology	25일(화), 09:00-11:00
<b>E. Compound Semiconductors</b>	<b>25일(화), 16:00-18:00</b>
F. Silicon and Group-IV Devices and Integration Technology	25일(화), 09:00-11:00
G. Device & Process Modeling, Simulation and Reliability	25일(화), 16:00-18:00
H. Display and Imaging Technologies	25일(화), 09:00-11:00
I. MEMS & Sensors Systems	25일(화), 16:00-18:00
J. Nano-Science & Technology	26일(수), 14:00-16:00
K. Memory (Design & Process Technology)	26일(수), 14:00-16:00
L. Analog Design	26일(수), 09:00-11:00
M. RF and Wireless Design	26일(수), 14:00-16:00
N. VLSI CAD	26일(수), 09:00-11:00
O. System LSI Design	26일(수), 09:00-11:00
P. Device for Energy (Solar Cell, Power Device, Battery, etc.)	26일(수), 09:00-11:00
Q. Metrology, Inspection, Analysis, and Yield Enhancement	26일(수), 15:30-17:30
R. Semiconductor Software	26일(수), 09:00-11:00
S. Chip Design Contest	
T. AI	26일(수), 09:00-11:00
U. Bio-Medical	26일(수), 14:00-16:00



# 제 29회 한국반도체학술대회

The 29th Korean Conference on Semiconductors

2022년 1월 24일(월)~ 26일(수) | 강원도 하이원 그랜드호텔(컨벤션타워)

2022년 1월 26일(수), 09:00-10:30

Room H (하트 I, 6층)

## E. Compound Semiconductors 분과 [WH1-E]

### Compound Semiconductor III

좌장: 김동현 박사(KANC)

<p><b>WH1-E-1</b> 09:00-09:15</p>	<p><b>3-levels-stacked In<sub>0.53</sub>Ga<sub>0.47</sub>As MBCFETs with Regrown S/D Contacts</b> In-Geun Lee<sup>1</sup>, Hyeon-Bhin Jo<sup>1</sup>, Sang-Tae Lee<sup>2</sup>, Minwoo Kong<sup>4</sup>, Ji-Min Baek<sup>1</sup>, Seung-Won Yun<sup>1</sup>, Hyeon-Seok Jeong<sup>1</sup>, Wan-Soo Park<sup>1</sup>, Ji-Hoon Yoo<sup>1</sup>, Su-Min Choi<sup>1</sup>, SangKuk Kim<sup>3</sup>, Jae-Gyu Kim<sup>3</sup>, Jacob Yun<sup>3</sup>, Ted Kim<sup>3</sup>, Tae-Woo Kim<sup>5</sup>, Dae-Hong Ko<sup>6</sup>, JungHee Lee<sup>1</sup>, Kwang-Seok Seo<sup>4</sup>, Chan-Soo Shin<sup>2</sup>, and Dae-Hyun Kim<sup>1</sup> <i><sup>1</sup>Kyungpook National University, <sup>2</sup>Korea Advanced Nano Fab Center, <sup>3</sup>QSI Inc., <sup>4</sup>Seoul National University, <sup>5</sup>University of Ulsan, <sup>6</sup>Yonsei University</i></p>
<p><b>WH1-E-2</b> 09:15-09:30</p>	<p><b>Trap Behavior of Metamorphic HEMTs with Pulsed IV and 1/f Noise Measurements</b> Ki-Yong Shin<sup>1</sup>, Ju-Won Shin<sup>1</sup>, Walid Amir<sup>1</sup>, Jae-Phil Shim<sup>2</sup>, Sang-Tae Lee<sup>2</sup>, Hyun-Chul Jang<sup>2</sup>, Kyung-Ho Park<sup>2</sup>, Chan-Soo Shin<sup>2</sup>, and Tae-Woo Kim<sup>1</sup> <i><sup>1</sup>School of Electrical, Electronic and Computer Engineering, University of Ulsan, <sup>2</sup>Korea Advanced Nano Fab Center</i></p>
<p><b>WH1-E-3</b> 09:30-09:45</p>	<p><b>Microcavity-integrated Flexible Mid-infrared Photodetector with Hetero-epitaxial Growth</b> Seungwan Woo<sup>1,2</sup>, Tae Soo Kim<sup>3</sup>, Jae-Hoon Han<sup>2</sup>, In-Hwan Lee<sup>1</sup>, Eung-Beom Yeon<sup>1,2</sup>, Daehwan Jung<sup>2</sup>, and Won Jun Choi<sup>2</sup> <i><sup>1</sup>Department of Materials Science and Engineering, Korea University, <sup>2</sup>Center for Optoelectronic Materials and Devices, KIST, <sup>3</sup>School of Electrical and Electronic Engineering, Yonsei University</i></p>
<p><b>WH1-E-4</b> 09:45-10:00</p>	<p><b>Metal Contact Optimization of Quantum Dot Laser for Epitaxial Lift-Off</b> Sung-Han Jeon<sup>1,2</sup>, Dae-Hwan Ahn<sup>1</sup>, Jindong Song<sup>1</sup>, Won Jun Choi<sup>1</sup>, Woo-Young Choi<sup>2</sup>, Daehwan Jung<sup>1</sup>, and Jae-Hoon Han<sup>1</sup> <i><sup>1</sup>Center for Opto-Electronic Materials and Devices, KIST, <sup>2</sup>Department of Electrical and Electronic Engineering, Yonsei University</i></p>
<p><b>WH1-E-5</b> 10:00-10:15</p>	<p><b>Fabrication and Characterization of Normally-Off <math>\beta</math>-Ga<sub>2</sub>O<sub>3</sub>Thin-Film Phototransistor with A Thickness of 8-nm</b> Youngbin Yoon<sup>1</sup>, Youngki Kim<sup>1</sup>, Wan Sik Hwang<sup>2</sup>, and Myunghun Shin<sup>1</sup> <i><sup>1</sup>School of Electronics and Information Engineering, Korea Aerospace University, <sup>2</sup>Department of Materials Engineering, Korea Aerospace University</i></p>
<p><b>WH1-E-6</b> 10:15-10:30</p>	<p><b>Trap Analysis of AlGaIn/GaN HEMT with Different Al Composition</b> Walid Amir<sup>1</sup>, Ju-Won Shin<sup>1</sup>, Ki-Yong Shin<sup>1</sup>, Surajit Chakraborty<sup>1</sup>, Jae-Moo Kim<sup>2</sup>, ChuYoung Cho<sup>2</sup>, Kyung-Ho Park<sup>2</sup>, Takuya Hoshi<sup>3</sup>, Takuya Tsutsumi<sup>3</sup>, Hiroki Sugiyama<sup>3</sup>, Hideaki Matsuzaki<sup>3</sup>, and Tae-Woo Kim<sup>1</sup> <i><sup>1</sup>Department of Electrical, Electronic and Computer Engineering, University of Ulsan, <sup>2</sup>Korea Advanced Nano Fab Center, <sup>3</sup>NTT Device Technology Laboratories, NTT Corporation</i></p>

# Metal Contact Optimization of Quantum Dot Laser for Epitaxial Lift-Off

Sung-Han Jeon<sup>1,2</sup>, Dae-Hwan Ahn<sup>1</sup>, Jindong Song<sup>1</sup>, Won Jun Choi<sup>1</sup>, Woo-Young Choi<sup>2</sup>, Daehwan Jung<sup>1</sup> & Jae-Hoon Han<sup>1\*</sup>

<sup>1</sup>Center for Opto-Electronic Materials and Devices, Korea Institute of Science and Technology (KIST),

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A quantum dots (QD) laser is a promising solution of on-chip light source for an integrated photonics platform thanks to its low threshold current density and high thermal stability [1]. To integrate high-performance QD lasers on Si, we have investigated the wafer bonding with epitaxial lift-off (ELO) technique on a Si platform (Fig. 1) [2]. In this report, we investigate metallization conditions for ELO wafer bonding with smooth surface morphology and low contact resistance. To confirm the contact resistance and the surface roughness, palladium (Pd), molybdenum (Mo), tungsten (W), and nickel (Ni) were deposited on a p+GaAs wafer and annealed for 1 minute. We confirmed that ohmic junction were not formed at the interfaces for Mo/p+GaAs and W/p+GaAs under the 250~400 °C temperature. In the case of Ni, although an ohmic condition was obtained after annealing over 300 °C, the root mean square (RMS) value for the surface roughness exceeded 1 nm; thus, it is not suitable for further wafer bonding process. Figure 2 shows the measured contact resistance and RMS surface roughness of Pd under various annealing temperatures for 1 min. Although the contact resistance decreased under higher annealing temperature, RMS values were degraded over 300 °C. Therefore, we chose the annealing temperature of 250 °C, which has relatively good contact resistance of  $9.19 \mu\Omega \cdot \text{cm}^2$  with a low RMS value of 0.563 nm. We also confirmed the wafer bonding process using Pd/Au bonding metal on QD-LD epitaxy substrate and p+Si substrate. Before bonding, a QD laser wafer was annealed for 1 min after Pd/Au deposition. Then, p+Si wafer and QD laser wafer were bonded at room temperature. Finally, QD laser structure was transferred on the Si substrate by ELO technique from the GaAs substrate using HF solution as shown in Fig. 3. This metallization condition is a promising solution for high-yield and low-resistivity wafer bonding with ELO technique for a QD laser on the Si platform.

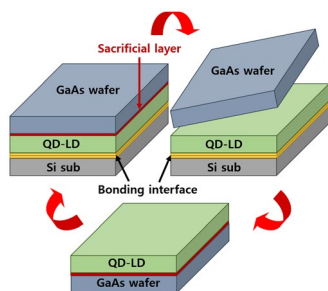


Fig 1. Schematics of wafer bonded QD laser on Si using metal bonding with ELO technique.

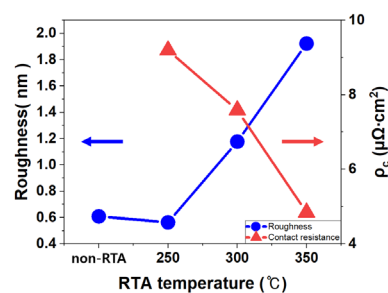


Fig 2 Contact resistance and RMS value of Au/Pd/p+GaAs after annealing.

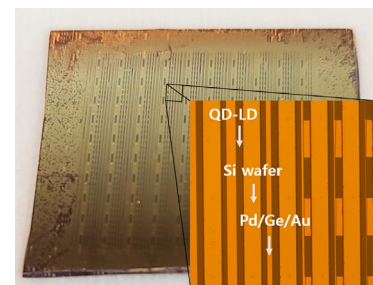


Fig 3. Image of wafer-bonded QD laser on Si using metal bonding with ELO technique.

**Acknowledgments** This work was supported in part by the Institutional Program (2E31011) funded by the Korea Institute of Science and Technology (KIST), and in part by the National Research Foundation of Korea (NRF) Grant funded by the Korean Government Ministry of Science and ICT under Grant 2017M1A2A2048904, 2019M3F3A1A0207206912

**References** [1] Kenichi Nishi *et al.*, "Development of Quantum Dot Lasers for Data-Com and Silicon Photonics Applications," *IEEE J. Sel. Top. Quantum Electron* 23, vol 1, 2017. [2] Jae-Hoon Han, The 29th Korean Conference on Semiconductors (2021).