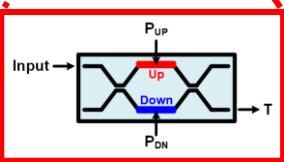
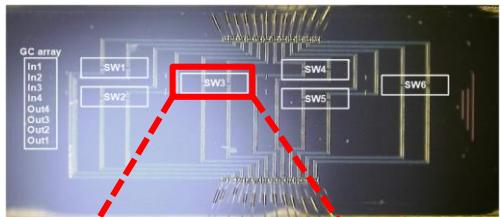
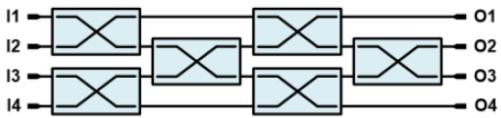


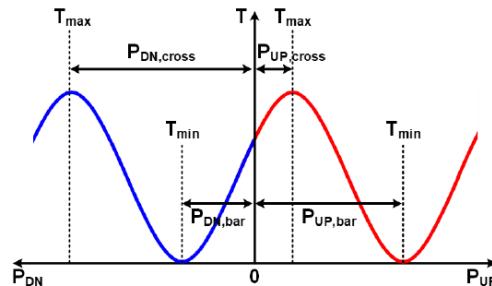
Bias Controller for Si Optical Switch Fabrics

Target PIC

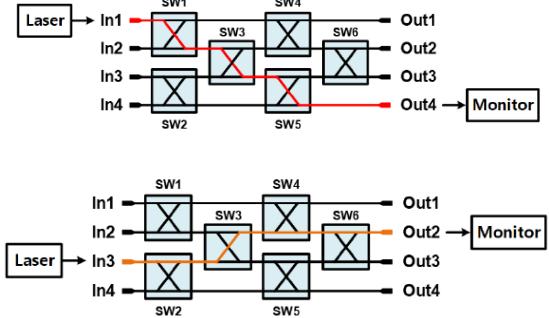


< Chip microphotograph >

Calibration scheme

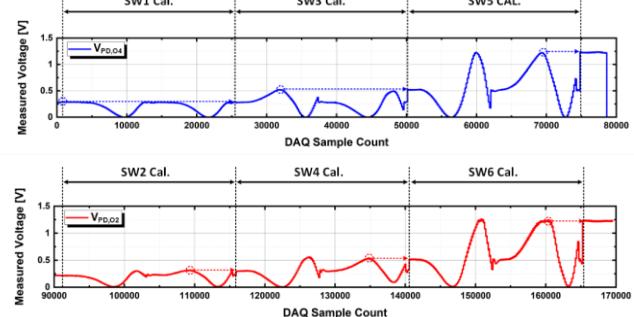
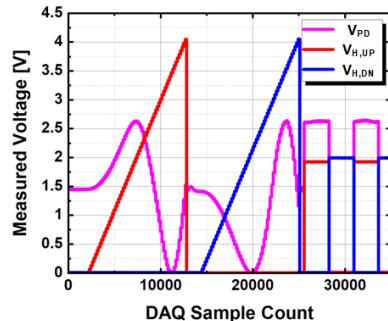


➤ Power efficient heater selection



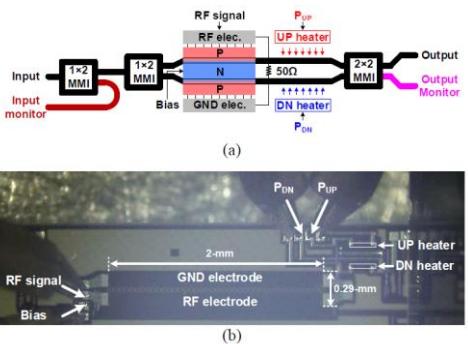
➤ Sequential calibration can be done

Measurement

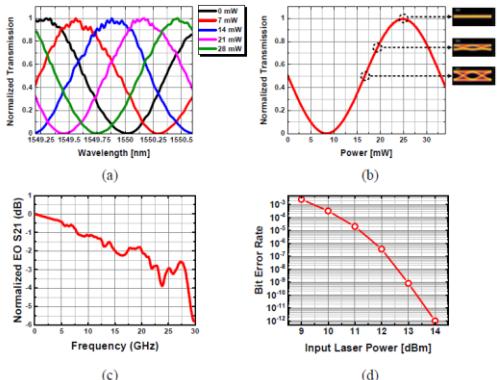


Bias Controller for Si MZM

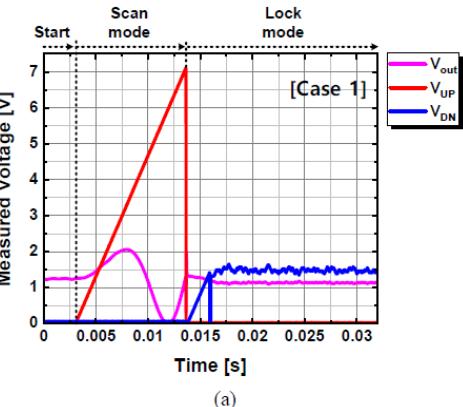
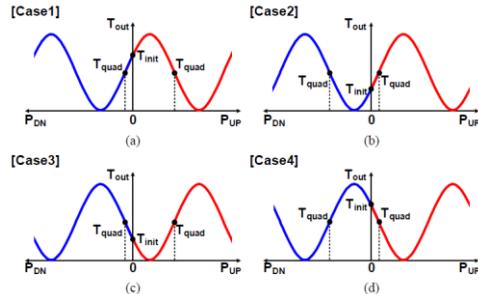
Target PIC



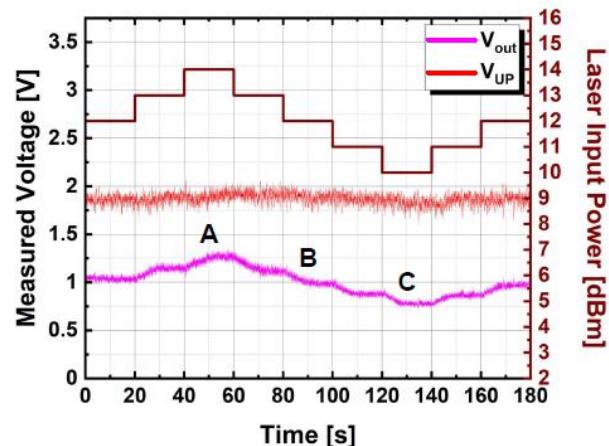
< Si single push-pull MZM >



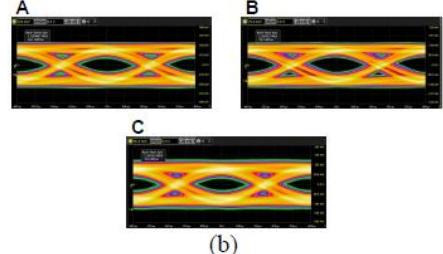
Calibration scheme



Measurement



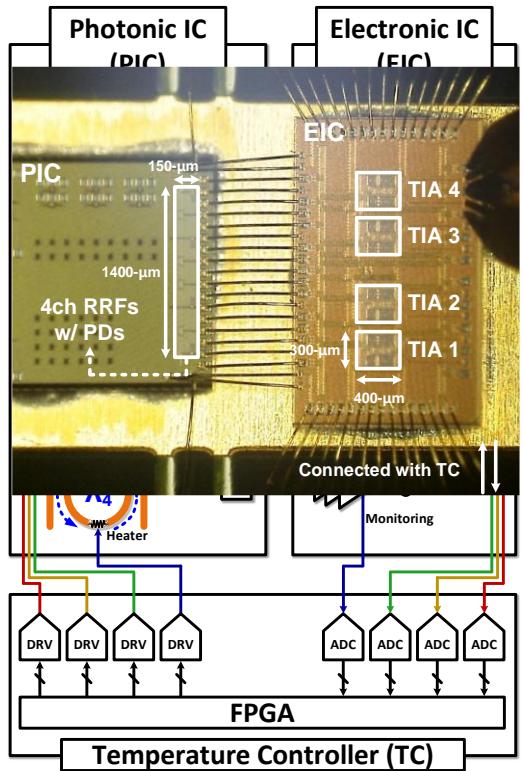
(a)



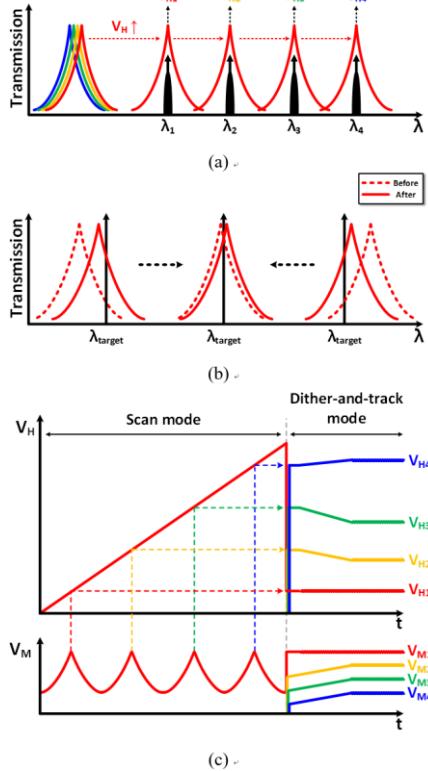
(b)

Temperature controller for RRF based WDM Rx

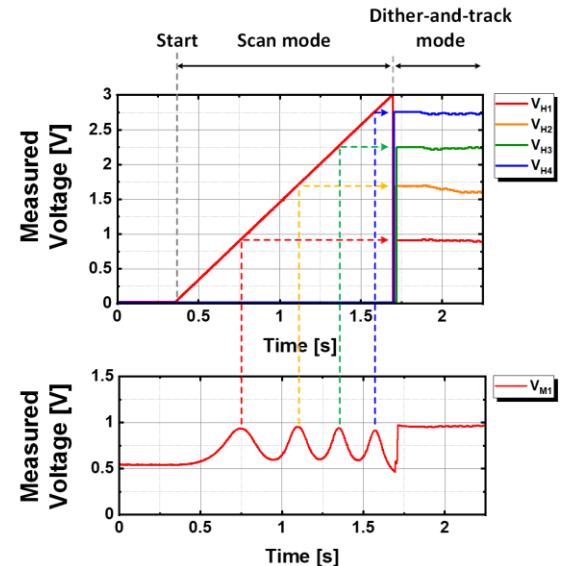
Designed Chip



Calibration scheme



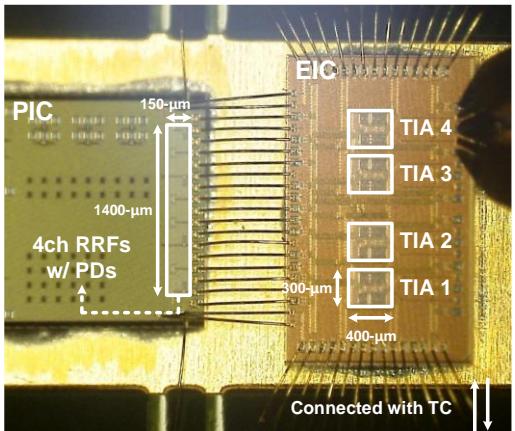
Measurement



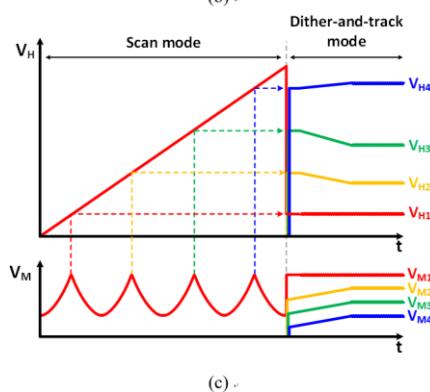
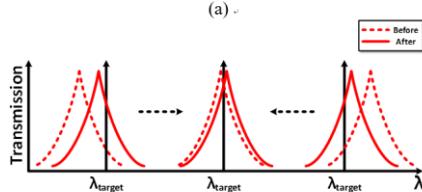
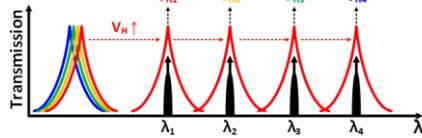
➤ Initial calibration

Temperature controller for RRF based WDM Rx

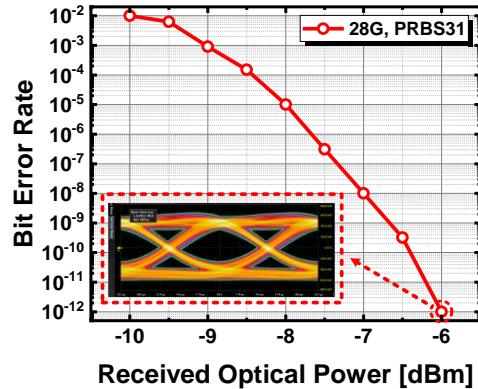
Designed Chip



Calibration scheme



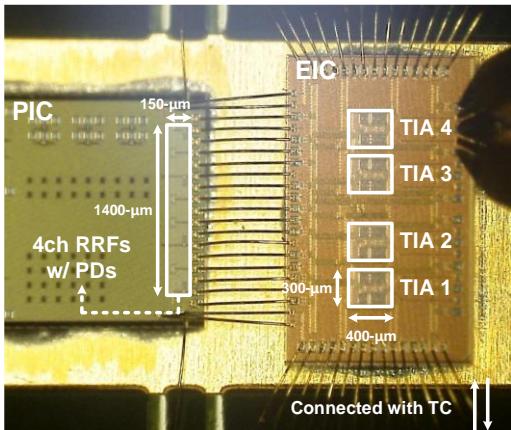
Measurement



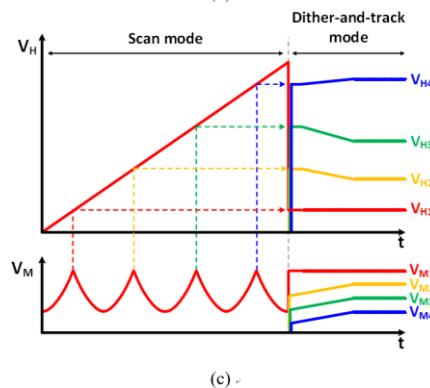
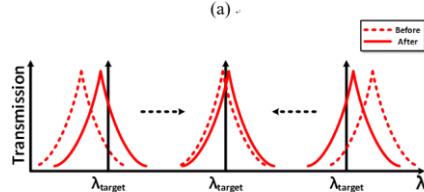
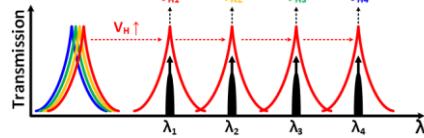
➤ ORx performance

Temperature controller for RRF based WDM Rx

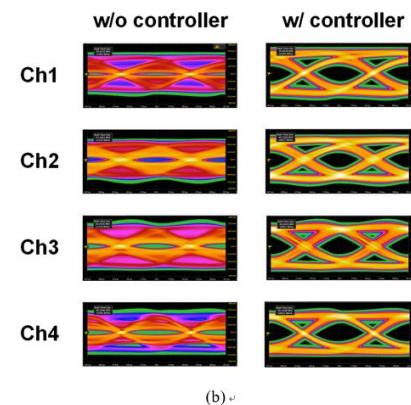
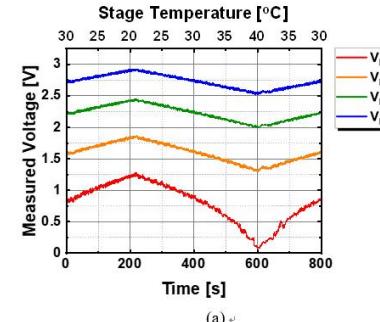
Designed Chip



Calibration scheme



Measurement

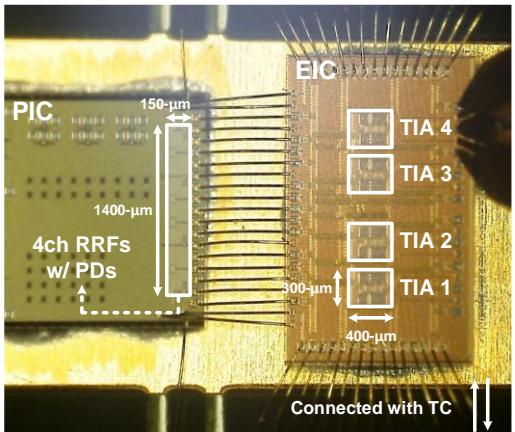


➤ Thermal stress test

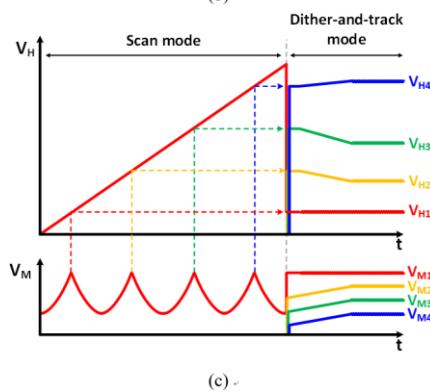
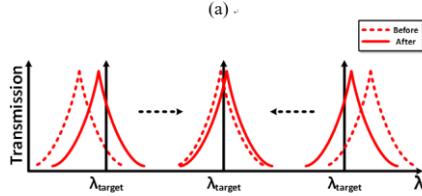
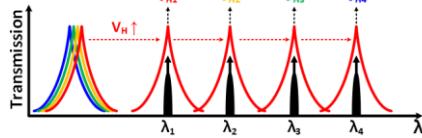
Preparing publish

Temperature controller for RRF based WDM Rx

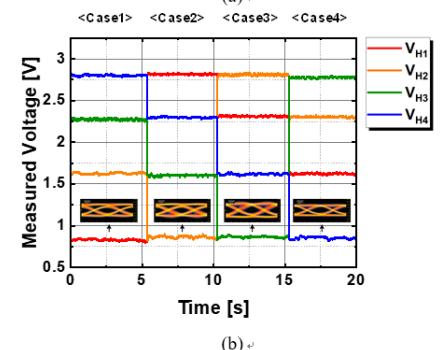
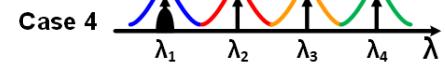
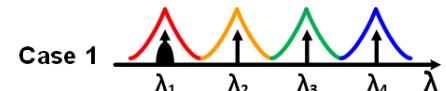
Designed Chip



Calibration scheme



Measurement



➤ WDM reconfiguration

Preparing publish