

The background of the slide features a large, light blue watermark of the Yonsei University seal. The seal is circular with the text 'YONSEI UNIVERSITY' at the top and '연세대학교' at the bottom. In the center is a shield with a book, a lamp, and a central circle, with the year '1885' at the bottom.

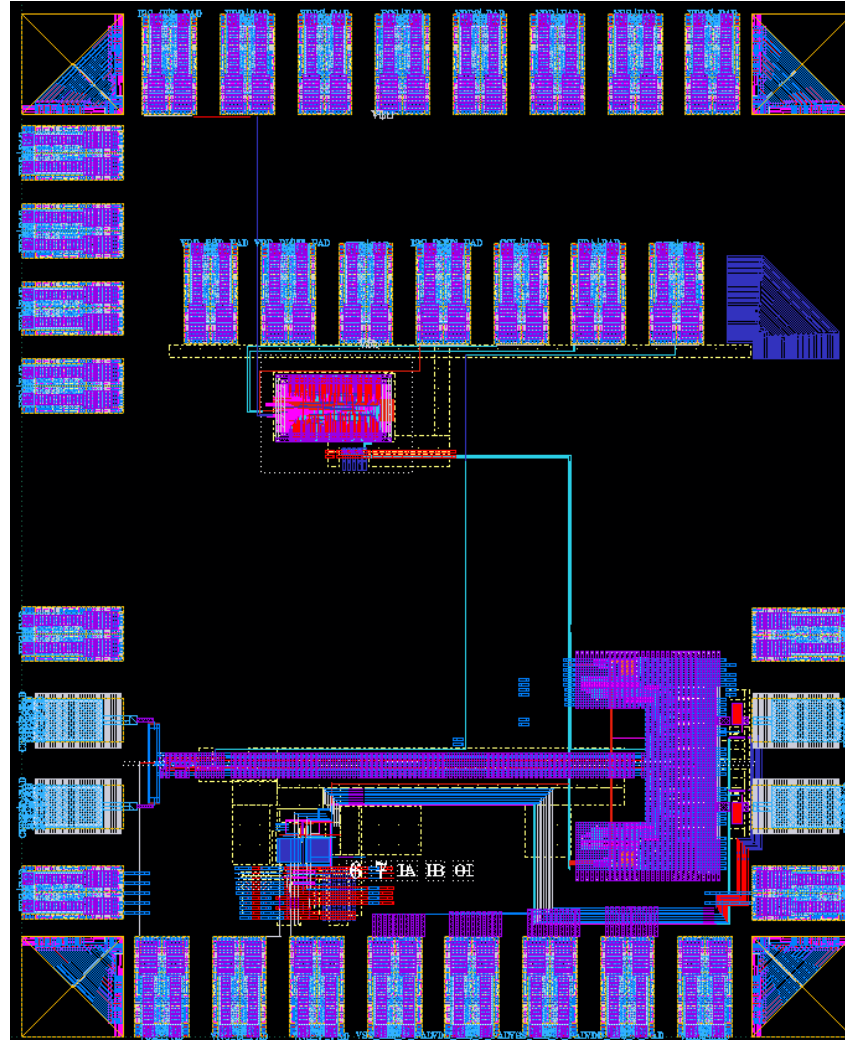
2025 Workshop

Jaewon Lee

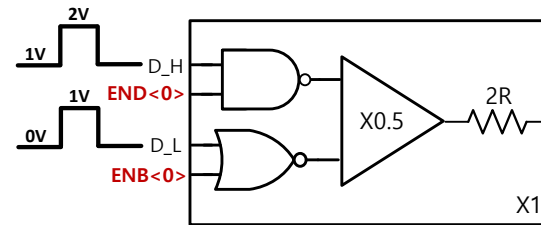
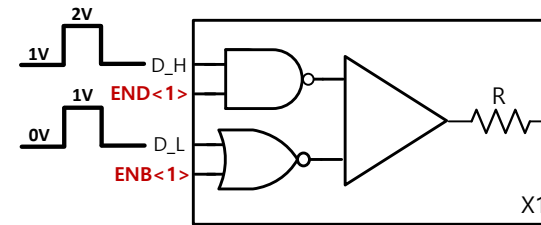
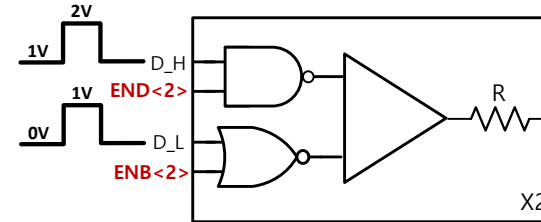
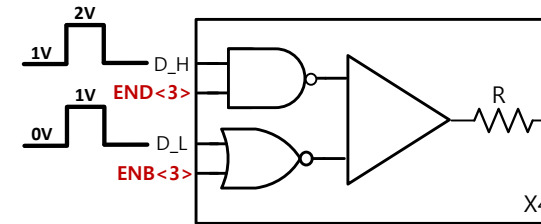
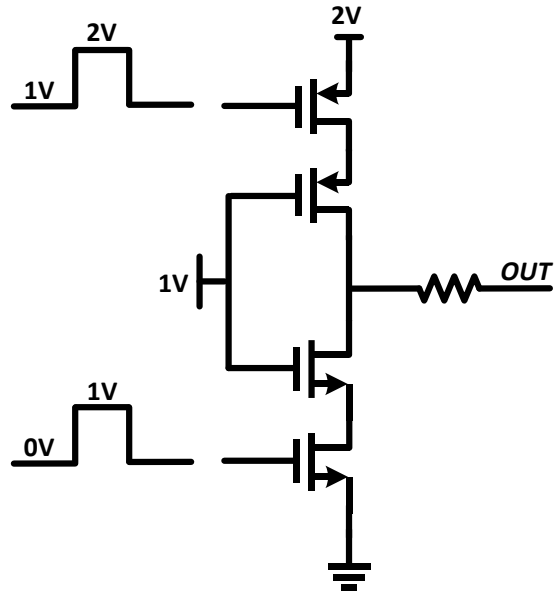
**High-Speed Circuits & Systems Lab.
Dept. of Electrical and Electronic Engineering
Yonsei University**

2025-2 Work

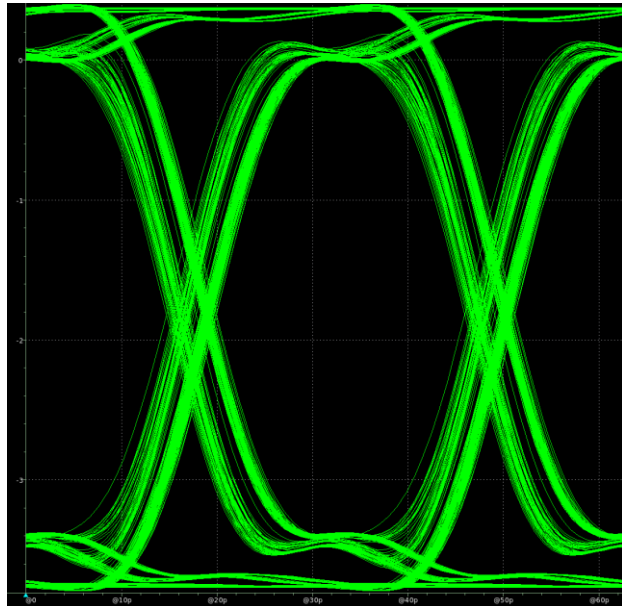
$1\text{-}\lambda \times 32\text{-Gb/s}$ MRM OTx



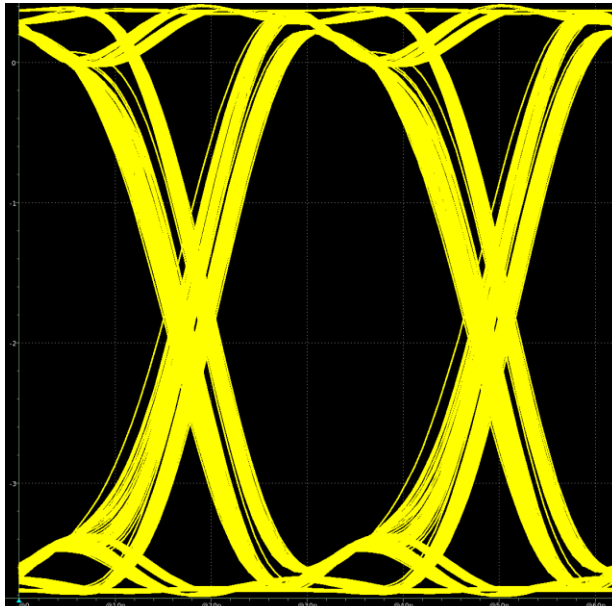
Driver



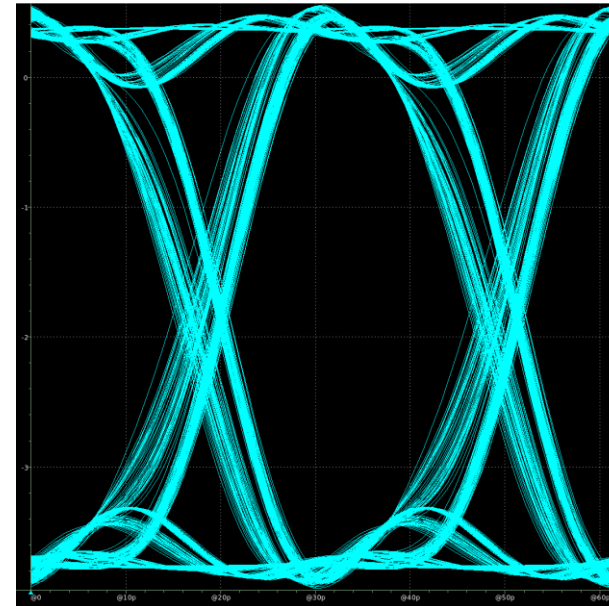
Wire Bonding Inductance



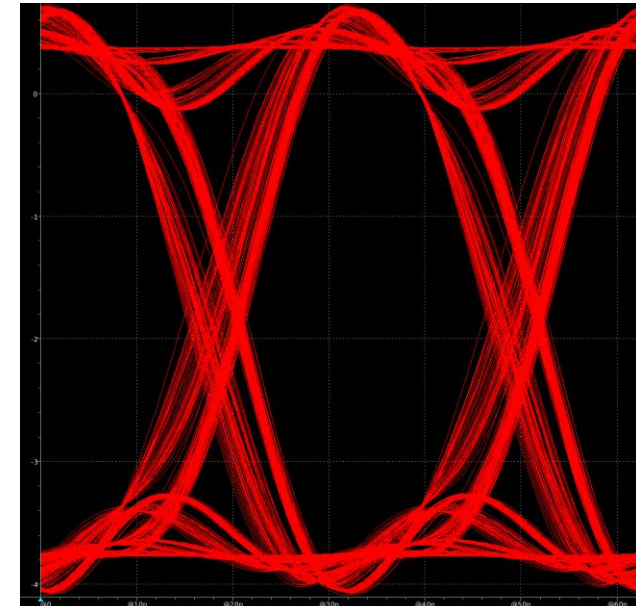
L:0.565nH → 678um



L:0.795nH → 896um



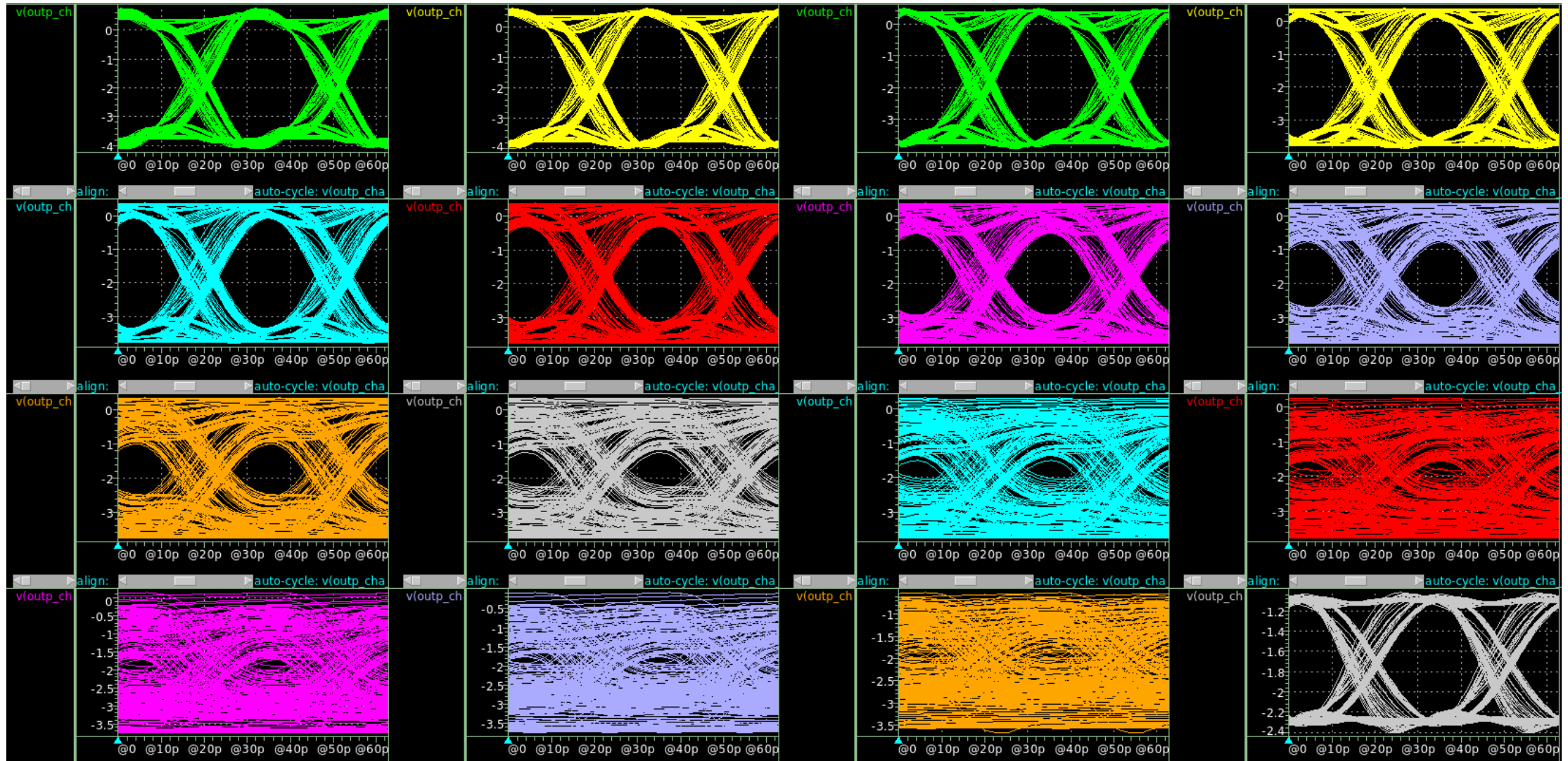
L:0.970nH → 1054um



L:1.162nH → 1223um

DRV R Control

R : 38~620Ω



Plan

- ETx study
- 2601 Driver measurement
- ETx w/ 세환 IDEC 28nm MPW(7월 예정)