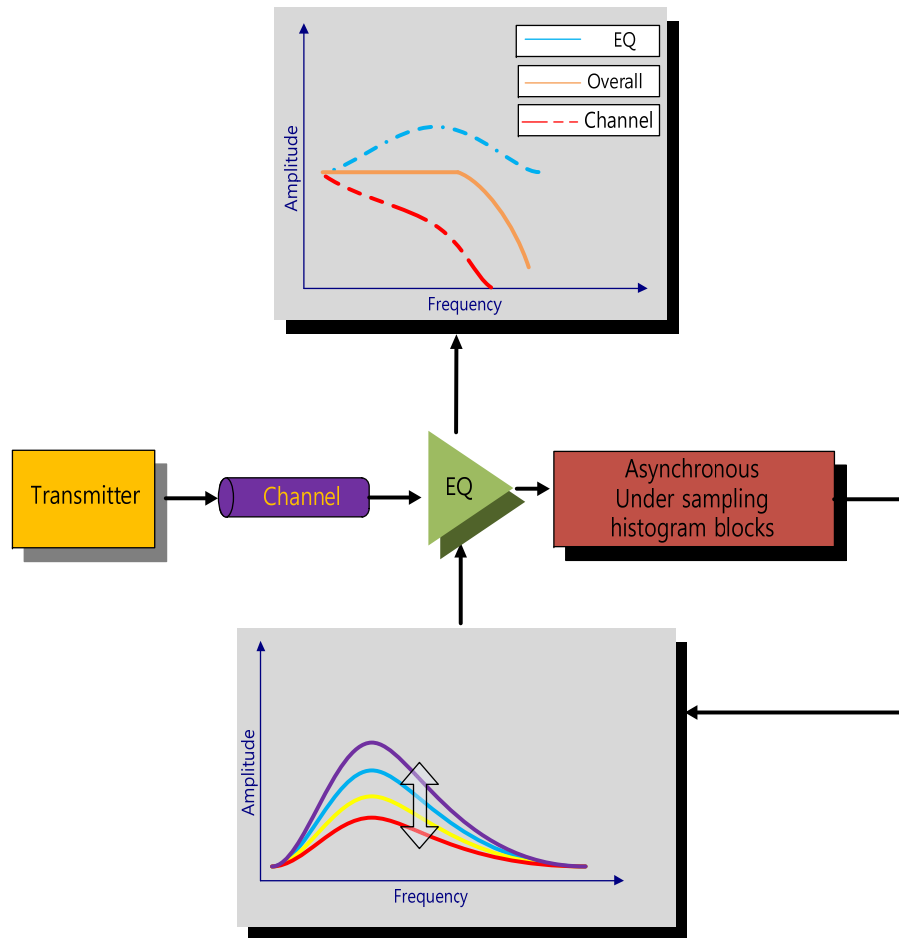
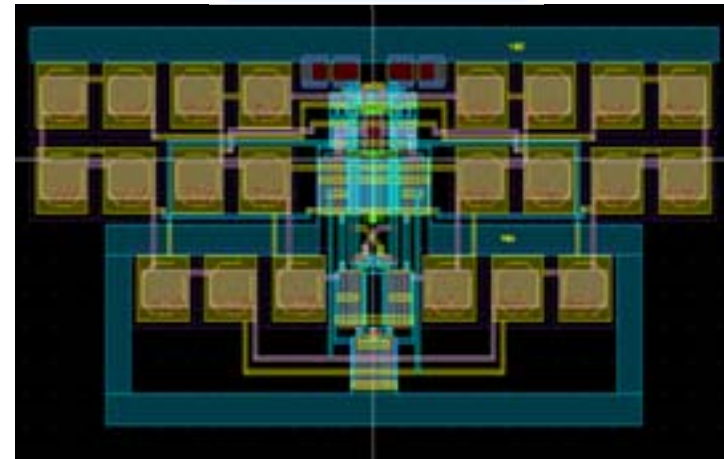


Designing high speed equalizer for Serial Interface



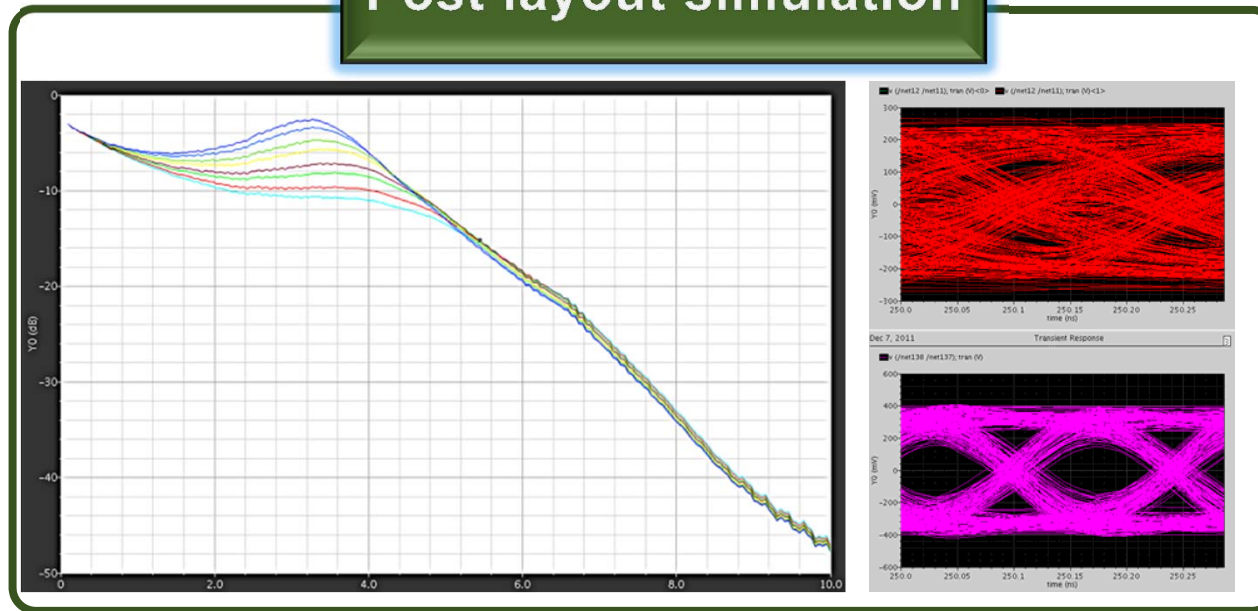
Layout



- RC source degeneration filter with negative capacitor is used for high frequency boosting
- Deciding the boosting extent, by the asynchronous under sampling histogram blocks
- Boosting range : 5 ~ 15 dB

High Speed Equalizer for Serial Interface

Post layout simulation



- At the post layout simulation, equalization of 8 Gb/s data rate is successfully achieved
- According to the output code of the histogram algorithm, high frequency boosting can cover from 5~15 dB